at the specific value or within the specific range after the start of a protective operation of an internal power supply circuit that generates said drive voltages;

a second drive voltage decision unit determining whether or not said drive voltages are kept at the specific values or within the specific ranges; and

a drive control signal control unit controlling drive control signals of said flat plasma display in response to the decided results of said first and second high voltage decision units and said first and second drive voltage decision units.

2. (AS UNAMENDED) A flat plasma display as claimed in claim 1, wherein the control of said internal power supply circuit is carried out together with the control of said drive control signals in response to the decided results of said second drive voltage decision unit.

3. (AS UNAMENDED) A flat plasma display as claimed in claim 1, wherein said flat plasma display is initialized when said second high voltage decision unit determines that said high voltage is not kept at the specific value or the specific range, and an internal power of said internal power supply circuit and said drive voltages are cut OFF when said second drive voltage decision unit determines that said drive voltages are not kept at the specific values or within the specific ranges.

4. (AS UNAMENDED) A flat plasma display as claimed in claim 1, wherein said flat plasma display further comprises a time compensation unit for compensating for the time between the instant that said high voltage is applied until said drive voltages reach the specific values.

5. (AS UNAMENDED) A flat plasma display as claimed in claim 1, wherein said specific value compared with said high voltage in said first high voltage decision unit differs from said specific value compared with said high voltage in said second high voltage decision unit.

6. (AS UNAMENDED) A flat plasma display as claimed in claim 1, wherein said flat plasma display comprises a three-electrode surface discharge AC plasma display.

7. (AS UNAMENDED) A flat plasma display as claimed in claim 6, wherein said three-electrode surface discharge AC plasma display further comprises:

first and second electrodes arranged in parallel with each other; and

third electrodes orthogonal to said first and second electrodes, said first electrodes being commonly connected together and said second electrodes being arranged to define respective display lines, wherein said display has a surface discharge structure employing wall charges as a memory.

8. (AS UNAMENDED) A flat plasma display as claimed in claim 7, wherein said three-electrode surface discharge AC plasma display further comprises:

a first substrate, said first and second electrodes being arranged in parallel to each other on said first substrate and paired for defining respective display lines;

a second substrate spaced apart from and facing said first substrate, defining a cavity therebetween, said third electrodes being arranged on said second substrate in orthogonal relationship to said first and second electrodes and displaced therefrom;

wall charge accumulating dielectric layers respectively covering the surfaces of said first and second electrodes;

a phosphor formed over said second substrate;

a discharge gas sealed in the cavity between said first and second substrates; and

cells formed at intersections where said first and second electrodes cross said third electrodes.

9. (ONCE AMENDED) A flat plasma display employing a first high voltage for supplying a sustain pulse, comprising:

a voltage detection unit detecting said first high voltage and other drive voltages which are produced by said first high voltage;

an internal power supply circuit receiving said first high voltage and generating a second high voltage different from said first high voltage; and

an internal power supply controlling unit producing power supply control signals controlling an operation of said internal power supply circuit in response to said detected first high voltage and other drive voltages.

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10. (AS UNAMENDED) A flat plasma display as claimed in claim 9, wherein said internal power supply controlling unit stops the operation of said internal power supply circuit by changing said power supply control signals in response to said detected first high voltage and other drive voltages.

11. (ONCE AMENDED) A flat plasma display employing a first high voltage for supplying a sustain pulse, comprising:

a voltage detection unit detecting said first high voltage;

an internal power supply circuit receiving said first high voltage and generating another high voltage different from said first high voltage; and

an internal power supply controlling unit storing first and second specific values and selectively comparing the stored first and second specific values with said detected first high voltage, said first specific value being used when said first high voltage is rising and said second specific value being used when said first high voltage is falling, and controlling an operation of the internal power supply circuit in response to the compared result of said detected first high voltage.

12. (AS UNAMENDED) A flat plasma display as claimed in claim 11, wherein said internal power supply controlling unit starts a circuit operation through a control circuit if said detected first high voltage reaches said first specific value, and stops the circuit operation through said control circuit if said detected first high voltage is below said second specific value.

13. (ONCE AMENDED) A flat plasma display, comprising:

a signal detection unit detecting a specific signal input from an external source;

an internal power supply circuit generating a plurality of drive voltages; and

an internal power supply controlling unit producing power supply control signals and stopping an operation of the internal power supply circuit by changing the power supply control signals, in response to said detected specific signal.

14. (ONCE AMENDED) A flat plasma display, comprising:

a display data checking unit checking display data input to said flat plasma display from an external source;

an internal power supply circuit generating a plurality of drive voltages; and

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an internal power supply stopping unit controlling an operation of the internal power supply circuit by changing power supply control signals, in response to the result of checking said display data.

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15. (ONCE AMENDED) A flat plasma display, comprising:

an internal power supply circuit generating a plurality of drive voltages;

an internal power supply controlling unit producing power supply control signals controlling an operation of said internal power supply circuit;

an external signal detection unit detecting a specific signal input to said plasma display from an external source; and

a drive control signal control unit controlling drive control signals of said plasma display in response to said detected specific signal.

16. (AS UNAMENDED) A flat plasma display as claimed in claim 15, wherein said internal power supply controlling unit controls an operation of said internal power supply circuit by changing said power supply control signals in response to said detected specific signal.

17. (AS UNAMENDED) A flat plasma display as claimed in claim 15, wherein said drive control signal control unit controls an operation of a display panel driving unit by changing said drive control signals in response to said detected specific signal.

18. (AS UNAMENDED) A flat plasma display as claimed in claim 15, wherein said control signal control unit and said internal power supply controlling unit stop operating if said specific signal is at a first level and start operating if said detected specific signal is at a second level, and thereby the drive control signals are controlled in response to a level of said detected specific signal.

19. (ONCE AMENDED) A flat plasma display comprising a three-electrode surface discharge AC plasma display, comprising:

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an external signal detection unit detecting a specific signal input to said flat plasma display from an external source; and

a drive control signal control unit controlling drive control signals of said flat plasma display to control a plurality of drive voltages of said flat plasma display in response to said